

Notes on the data processing algorithms for Lyrtech cards.

ADC data into Custom code are 14 bits. D15 is connected to overflow and D14 copies D3 (sign). All 16 bits go into the trace and raw data. However, only 14 bits (d13:0) are used for MWD, and CFD code.

Signed to Unsigned code at start of MWD interprets the 14 ADC data bits D13:0 like this:

NB Positive and negative refer to data at the ADC which is same polarity as the front panel input in Lyrtech VHS-ADC (GRT4 inverts data between input and ADC).

	ADC data from ADC	If “positive” at ADC (pol = 0)	If “negative” at ADC (pol = 1)
+ve FS	01 1111 1111 1111	0x3FFF	0x0000
0	00 0000 0000 0000	0x2000	0x1FFF
0	11 1111 1111 1111	0x1FFF	0x2000
-ve FS	10 0000 0000 0000	0x0000	0x3FFF

So now there is no sign bit, but the data increase in the direction of the signal's increase.

Bipolar signals are handled correctly because 0v is coded as 0x2000 in both cases.

Processing of the data uses the Georgiev/Gast MWD code based on a VHDL implementation by Martin Lauer¹ (in collaboration with Ian Lazarus) for the GRT4 VME cards.

Two copies of the so-called MWD code operate on the result of the signed2unsigned process. One has minimal shaping times to act like a TFA and its output is fed into a CFD algorithm with fixed fraction (0.5) and fixed 50ns delay. The threshold is programmable. The second copy of the MWD code has programmable parameters and is used for energy calculation as follows:

Processing of data:

- Signed2unsigned produces 14 bits unsigned values 0 to 0x3FFF.
(Input is ADC(13:0); Output is unsignedout(13:0) which feeds MWDin of mwdinstanz module which starts with decimation.)
- Decimation: increases 14 to 18 bits, no possible sign change: unsigned values 0 to 0xFFFFC.
(Input port called MWDin(13:0) receives MWDin(13:0) signal. Output port is DEC2MAUsig(17:0). Decimation factor is 4 (was 16 at times in the past, hence 18 bits out when only 16 are needed or used.).
- Moving window deconvolution (calculation is $x[n]-x[n-M]+area*coeff$).
Use MAU to output delayed and mirrored copies of the DEC2MAU(15:0) signal. Subtract delayed value from current value and save both the difference and the carry bit. Within the MAU, accumulate the differences to make a 24 bit value, MAU2NORMsig(23:0) signal which represents the area under the pulse over integration length M, the MWD window. While the differences may be negative due to noise or on the tail of the pulse, the accumulated value (area) should always be positive because the input is always positive as a

¹ Martin Lauer's PhD Thesis, 2004 "Digital Signal Processing for segmented HPGe Detectors: Preprocessing Algorithms and Pulse Shape Analysis" Available from: <http://cdsweb.cern.ch/record/811708?ln=en>

result of the signed2unsigned unit. Maximum for MAU2NORM is $256 \times 0xFFFC = 0xFFFC00$. Subout can be + or - 65535 (0xFFFF).

MAU input port receives DEC2MAU(15:0) and shape_time controls difference and size of accumulated result. Output is called MAU2NORMSIG(23:0). The external subtraction unit takes 16 bits signals mau_inst and mau_del from MAU. After subtraction the difference is output as subout(15:0) with the carry (sign) bit in subout(16). To match the 24 bits of the multiplication, the subout signal becomes newsubout(23:0) where lowest 7 bits are 0.

- Multiplication to correct for decay time constant. The accumulator output is scaled by a multiplication factor derived from the decay time constant, the shaping time and the clock: $\text{decaytime} = \text{coeff} / \text{length} = (1 - \exp(-M/\tau)) / (M/dT)$. See table following which lists permissible combinations of Tau and M. Any value of Tau >20us is OK and cannot cause algorithm overflow. The ALU output is 24.24 bits but in fact can only be 16.24 because of the normalisation (coefficient depends how many samples we are correcting for, so must divide by M/dt) so we ignore the 8 MSBs (always 0) and include 8 of the 24 fractional bits making a 16.8 number (actually signed 15.8 with sign forced to 0) made up from bits 39 down to 17 of the multiplier output (23 bits) with the 24th bit being forced to 0 because we need a signed value to add to the signed difference in the last part of the MWD: $x[n] - x[n-M] + \text{area} * \text{coeff}$
Inputs are MAU2NORMSIG(23:0) and decaytime(23:0) resulting in ALUout(47:0) which becomes NewALU(23:0) with NewALU(23) = 0 and NewALU(22:0) = ALUout(39:17).
- Final part of deconvolution- signed addition of signed difference and unsigned corrected area resulting in a square pulse with a flat top (if the shaping time correction is done right).
Inputs are newsubout(23:0) and NewALU(23:0) with outputs MWDSIG(24:0) where MWDSIG(24) is the carry out (overflow) bit used to verify that the algorithm doesn't overflow/underflow and MWDSIG(23:0) is the signed value.
- Trapezoidal filter- Need to apply a low pass filter of length L to MWD result to reduce the noise. This is a simple "accumulate over a time window" moving average function and results in trapezoidal output (or triangular in the case that M= L). The subtraction element is signed because badly adjusted MWD can undershoot. Also need to allow 8 extra bits for the accumulation based on a 24 bit input signal. The output trapezoid should, however, be positive in the range 0 to 0x3FFFFFFF (max value of MWDsig is 0x7FFFFFFF because its 23 bits+sign, so max for trapezsig is 23+8 = 31 bits+sign (nominally signed but normally +)).
Input is MWDSIG(23:0) with output trapezsig(31:0).
- Optional baseline subtraction. The baseline is calculated by averaging the trapezsig signal between triggers and freezing the value for 2 shaping times following each trigger. By default the baseline is subtracted from the trapezoid value to give the pulse height but this can be turned off in the firmware. Baseline uses IIR exponential filter (there is also a MAU version which needs 8 bit values; iir needs only 4 so ignores top 4 bits). Values n= 0-15 represent 2^n in the iir filter, so 0 is no filtering, 4 is average of 16 values (typical value) and maximum 15 is 2^{15} averages over 32768 updates. The update parameter defines how many decimated clocks (40ns) elapse between updates, so a typical value of 50 = 2us. A filter with average = 4 (16) and update = 50 (2us) averages over a period of 32us and the worst case (ave = $255 \times 40\text{ns} = 10.2\text{us}$ x average =15 (32768)) averages over 0.33sec. This is the active time between pulses (the baseline is disabled for 2x shaping time, so at 10k cps and 8us shaping the baseline is disabled for a further $10\text{k} \times 16\text{us} = 160\text{ms}$).

