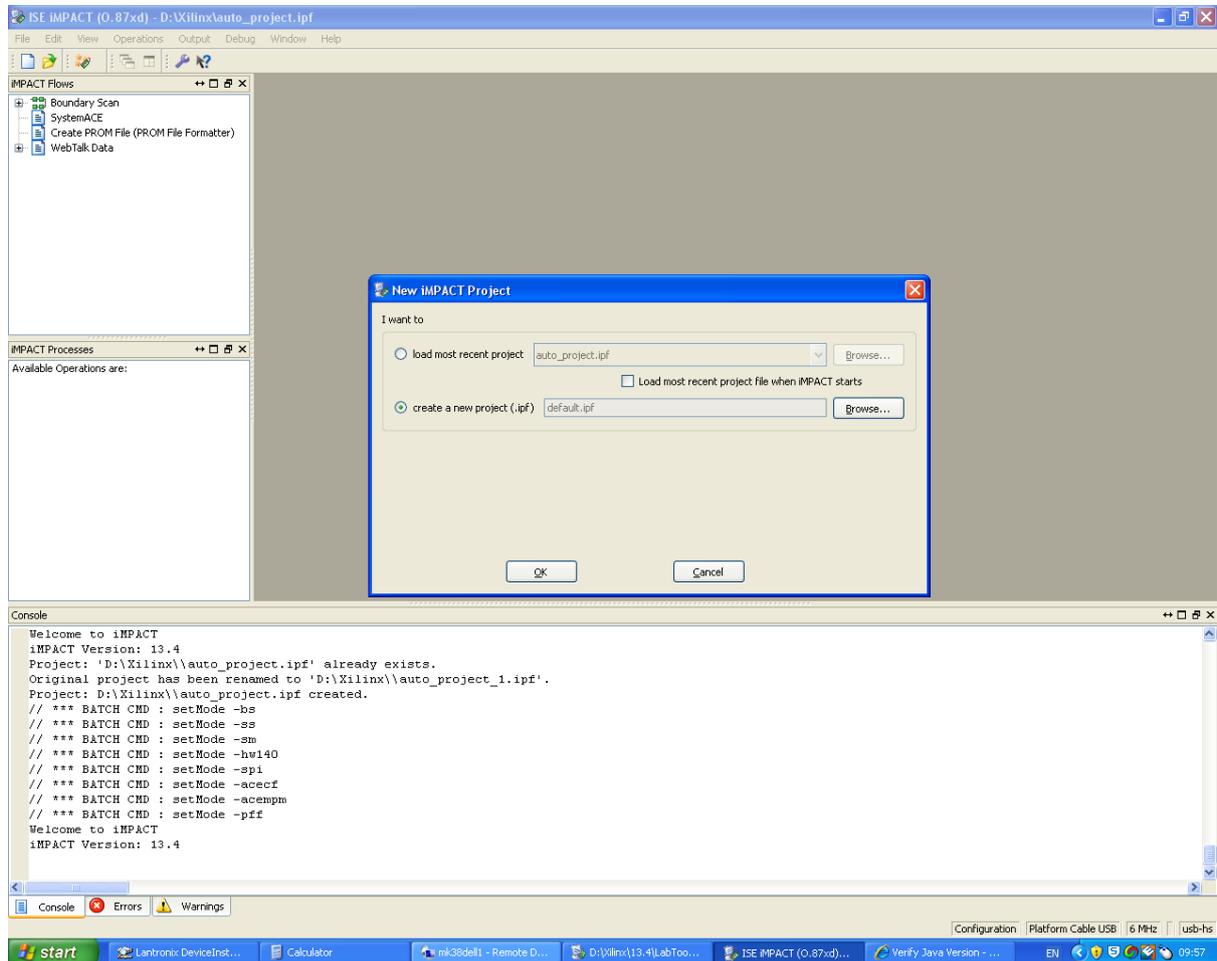
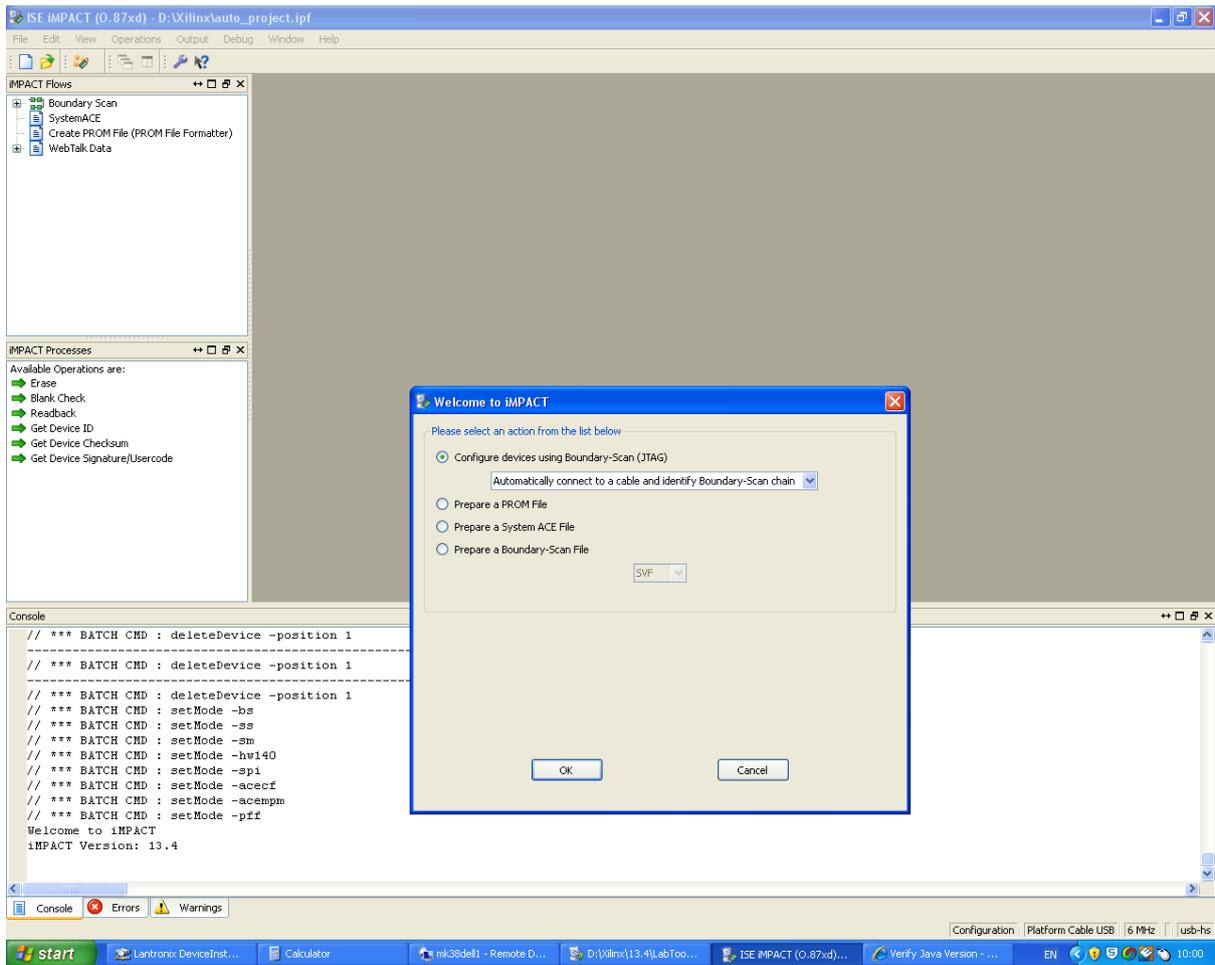


Program Xilinx Platform Flash via Impact

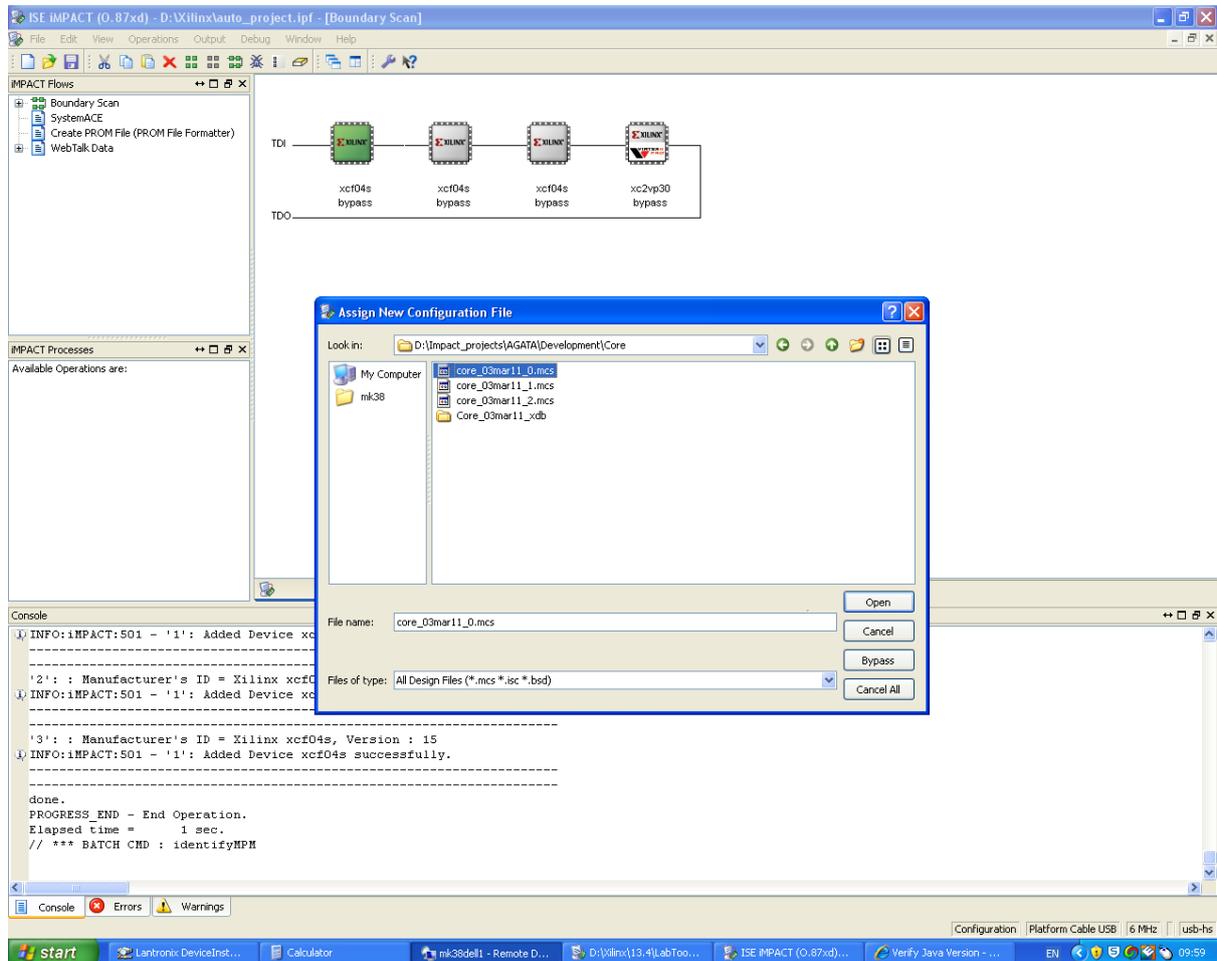
1. Open Impact , choose “create a new Project” and click OK:



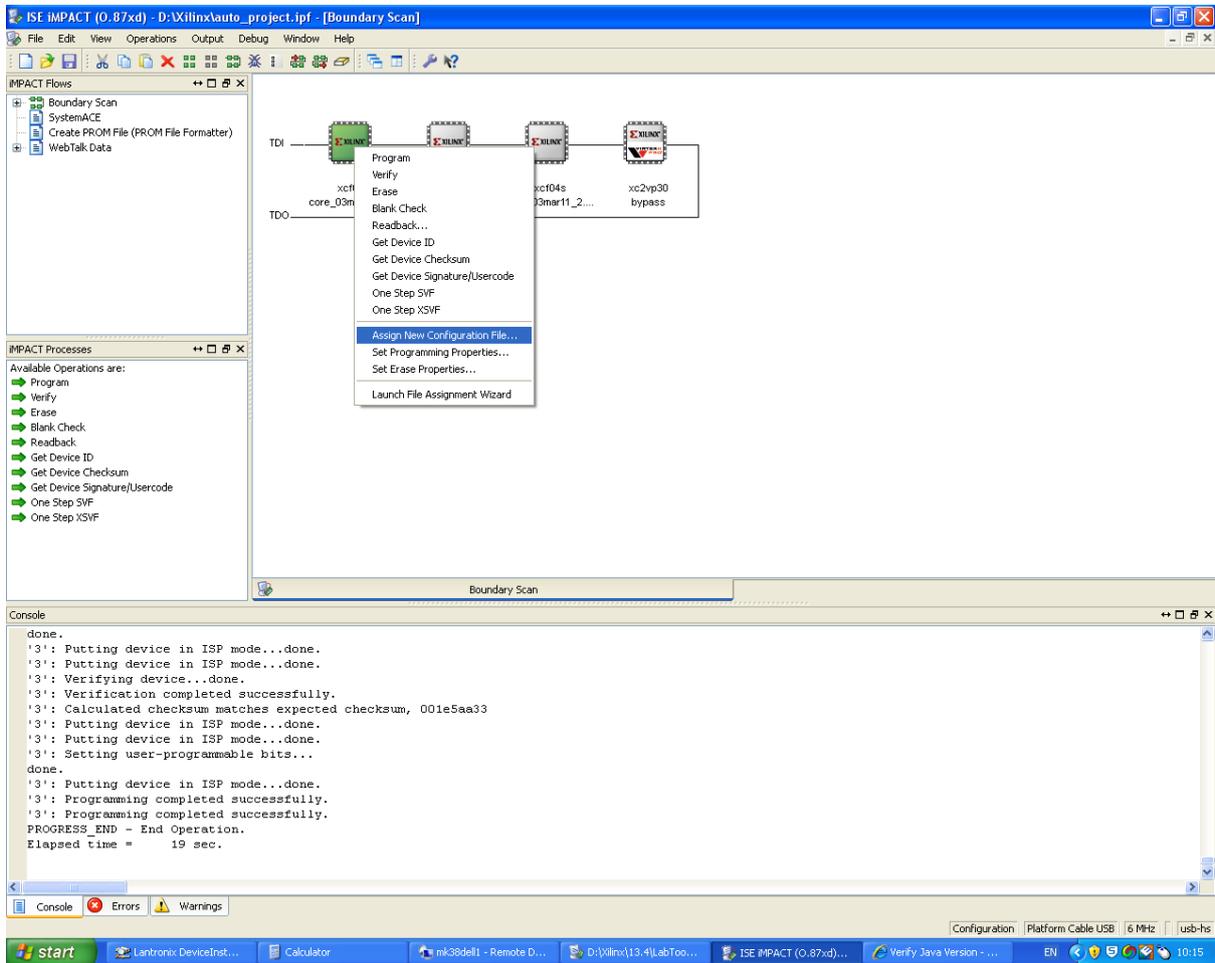
2. Choose "Configure devices using Boundary-Scan, as shown below:



- Go to the folder where the .mcs files (3 in all) and assign them to each of the 3 platform flashes, as shown below:



4. If the “Assign New configuration file” window does not come up automatically, then right click on each of the 3 platform flashes and select “Assign New configuration file”, as shown below:



5. Assign the 2nd .mcs file:

The screenshot displays the ISE IMPACT (0.87xd) interface. The main window shows a configuration flow for a Boundary Scan session. The flow consists of four devices connected in a line between TDI and TDO:

- Device 1: xc104s core_03mar11_0...
- Device 2: xc104s bypass
- Device 3: xc104s bypass
- Device 4: xc2vp30 bypass

The IMPACT Processes panel on the left lists available operations: Erase, Blank Check, Readback, Get Device ID, Get Device Checksum, and Get Device Signature/Usercode. The Console window at the bottom shows the following log output:

```
INFO:IMPACT:501 - '1': Added Device xc104s
-----
'3': Manufacturer's ID = Xilinx xc104s
INFO:IMPACT:501 - '1': Added Device xc104s
-----
done.
PROGRESS_END - End Operation.
Elapsed Time = 0 sec.
// *** BATCH CMD : identifyMPM
// *** BATCH CMD : assignFile -p 1 -file "D:/Impact_projects/AGATA/Development/Core/core_03mar11_0.mcs"
'1': Loading file 'D:/Impact_projects/AGATA/Development/Core/core_03mar11_0.mcs' ...
done.
// *** BATCH CMD : setAttribute -position 1 -attr packageName -value ""
```

An "Assign New Configuration File" dialog box is open, showing the file selection process. The "Look in:" field is set to "D:\Impact_projects\AGATA\Development\Core". The file list shows several files, with "core_03mar11_1.mcs" selected. The "File name:" field contains "core_03mar11_1.mcs". The "Files of type:" dropdown is set to "All Design Files (*.mcs *.jsc *.bsd)".

Click Ok below:

The screenshot shows the ISE IMPACT (0.87xd) interface. The main window displays a diagram of a JTAG chain with four devices: three xc104s core_03mar11_0... bypass and one xc2vp30 bypass. A warning dialog box is open, stating: "WARNING:IMPACT - The file 'D:/Impact_projects/AGATA/Development/Core/core_03mar11_1.mcs' does not appear to be a configuration file. This file will not configure a Xilinx device in Serial or SelectMap modes and may not be intended for configuration. If this is a cascaded PROM file, you may ignore this warning and proceed." The dialog has an "OK" button.

The console window shows the following output:

```
done.
PROGRESS_END - End Operation.
Elapsed time = 0 sec.
// *** BATCH CMD : identifyMPM
// *** BATCH CMD : assignFile -p 1 -file "D:/Impact_projects/AGATA/Development/Core/core_03mar11_0.mcs"
'1': Loading file 'D:/Impact_projects/AGATA/Development/Core/core_03mar11_0.mcs' ...
done.
// *** BATCH CMD : setAttribute -position 1 -attr packageName -value ""
// *** BATCH CMD : assignFile -p 2 -file "D:/Impact_projects/AGATA/Development/Core/core_03mar11_1.mcs"
'2': Loading file 'D:/Impact_projects/AGATA/Development/Core/core_03mar11_1.mcs' ...
WARNING:IMPACT - The file 'D:/Impact_projects/AGATA/Development/Core/core_03mar11_1.mcs' does not appear to be a configuration file.
This file will not configure a Xilinx device in Serial or SelectMap modes and may not be intended for configuration.
If this is a cascaded PROM file, you may ignore this warning and proceed.
```

6. Assign the 3rd .mcs file:

The screenshot displays the ISE IMPACT (0.87xd) interface. The main window shows a configuration flow diagram with four Xilinx PROM components connected in a line. The components are labeled as follows:

- Component 1: xcf04s, core_03mar11_0...
- Component 2: xcf04s, core_03mar11_1...
- Component 3: xcf04s, bypass
- Component 4: xc2vp30, bypass

The IMPACT Processes panel on the left lists available operations: Erase, Blank Check, Readback, Get Device ID, Get Device Checksum, and Get Device Signature/Usercode.

The Console window at the bottom shows the following output:

```
done.
PROGRESS_END - End Operation.
Elapsed time = 0 sec.
// *** BATCH CMD : identifyMPM
// *** BATCH CMD : assignFile -p 1 -fil
'1': Loading file 'D:/Impact_projects/
done.
// *** BATCH CMD : setAttribute -positi
// *** BATCH CMD : assignFile -p 2 -fil
'2': Loading file 'D:/Impact_projects/
WARNING:IMPACT - The file 'D:/Impact_projects/IGATA/Development/Core/core_03mar11_1.mcs' does not appear to be a configuration file.
This file will not configure a Xilinx device in Serial or SelectMap modeS and may not be intended for configuration.
done.
// *** BATCH CMD : setAttribute -position 2 -attr packageName -value ""
```

An "Assign New Configuration File" dialog box is open, showing the file selection process. The "Look in:" field is set to "D:\Impact_projects\IGATA\Development\Core". The file list shows:

- core_03mar11_0.mcs
- core_03mar11_1.mcs
- core_03mar11_2.mcs
- Core_03mar11_xdb

The "File name:" field contains "core_03mar11_2.mcs". The "Files of type:" dropdown is set to "All Design Files (*.mcs *.jsc *.bsd)".

The Windows taskbar at the bottom shows the system tray with the time 10:01 and various icons.

Click OK below:

The screenshot shows the ISE IMPACT (O.87xd) interface. The main window displays a Boundary Scan diagram with four devices: two xc104s cores, one xc104s bypass, and one xc2vp30 bypass. A warning dialog box is open, stating: "WARNING:IMPACT - The file 'D:/Impact_projects/AGATA/Development/Core/core_03mar11_2.mcs' does not appear to be a configuration file. This file will not configure a Xilinx device in Serial or SelectMap modes and may not be intended for configuration. If this is a cascaded PROM file, you may ignore this warning and proceed." The console window shows the following output:

```
'1': Loading file 'D:/Impact_projects/AGATA/Development/Core/core_03mar11_0.mcs' ...
done.
// *** BATCH CMD : setAttribute -position 1 -attr packageName -value ""
// *** BATCH CMD : assignFile -p 2 -file "D:/Impact_projects/AGATA/Development/Core/core_03mar11_1.mcs"
'2': Loading file 'D:/Impact_projects/AGATA/Development/Core/core_03mar11_1.mcs' ...
WARNING:IMPACT - The file 'D:/Impact_projects/AGATA/Development/Core/core_03mar11_1.mcs' does not appear to be a configuration file.
This file will not configure a Xilinx device in Serial or SelectMap modes and may not be intended for configuration.
If this is a cascaded PROM file, you may ignore this warning and proceed.
done.
// *** BATCH CMD : setAttribute -position 2 -attr packageName -value ""
// *** BATCH CMD : assignFile -p 3 -file "D:/Impact_projects/AGATA/Development/Core/core_03mar11_2.mcs"
'3': Loading file 'D:/Impact_projects/AGATA/Development/Core/core_03mar11_2.mcs' ...
WARNING:IMPACT - The file 'D:/Impact_projects/AGATA/Development/Core/core_03mar11_2.mcs' does not appear to be a configuration file.
This file will not configure a Xilinx device in Serial or SelectMap modes and may not be intended for configuration.
If this is a cascaded PROM file, you may ignore this warning and proceed.
```

Click on bypass, below:

The screenshot shows the ISE IMPACT (0.87xd) interface. The main window displays a configuration flow diagram with four components: three 'xc104s' devices (labeled 'core_03mar11_0...', 'core_03mar11_1...', 'core_03mar11_2...') and one 'xc2vp30' device labeled 'bypass'. The 'bypass' device is highlighted with a mouse cursor. An 'Assign New Configuration File' dialog box is open, showing the file selection process. The dialog box has a 'Look in:' field set to 'D:\Impact_projects\AGATA\Development\Core' and a file list containing 'Core_03Mar11.bit' and 'Core_03mar11_xdb'. The 'Files of type:' dropdown is set to 'All Design Files (*.bit *.rpt *.nky *.isc *.bsd)'. The 'File name:' field is empty. The 'By-pass' button is highlighted in yellow. The console window at the bottom shows the following text:

```

// *** BATCH CMD : setAttribute -posit
// *** BATCH CMD : assignFile -p 2 -fil
'2': Loading file 'D:/Impact_projects/
WARNING:IMPACT - The file 'D:/Impact.pr
This file will not configure a Xilinx
If this is a cascaded PROM file, you
done.
// *** BATCH CMD : setAttribute -posit
// *** BATCH CMD : assignFile -p 3 -fil
'3': Loading file 'D:/Impact_projects/
WARNING:IMPACT - The file 'D:/Impact_projects/AGATA/Development/Core/core_03mar11_2.mcs' does not appear to be a configuration file.
This file will not configure a Xilinx device in Serial or SelectMap modes and may not be intended for configuration.
If this is a cascaded PROM file, you may ignore this warning and proceed.
done.
// *** BATCH CMD : setAttribute -position 3 -attr packageName -value ""

```

Click Ok below:

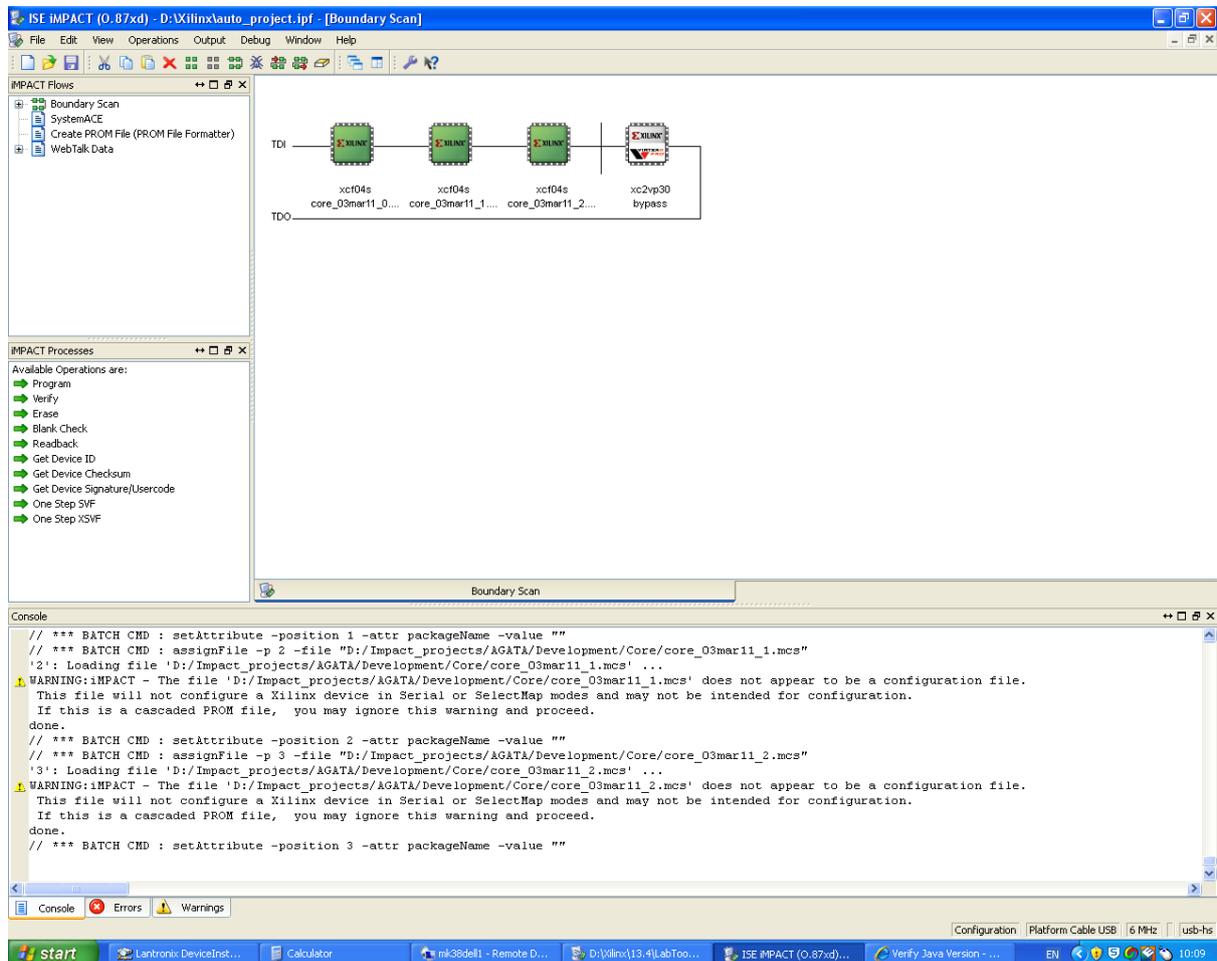
The screenshot shows the ISE IMPACT (O.87xd) interface. The main window displays a Boundary Scan configuration with four devices connected in a chain. The first three are xc104s (core_03mar11_0..., core_03mar11_1..., core_03mar11_2...) and the last is xc2vp30 (bypass). A dialog box titled "Device Programming Properties - Device 1 Programming Properties" is open, showing the "Boundary-Scan" category selected. The dialog contains a table of properties:

Property Name	Value
Verify	<input checked="" type="checkbox"/>
General CPLD And PROM Properties	
Design-Specific Erase Before Programming	<input checked="" type="checkbox"/>
Read Protect	<input type="checkbox"/>
PROM/CoolRunner-II Usercode (8 Hex Digits)	
PROM Specific Properties	
Load FPGA	<input type="checkbox"/>

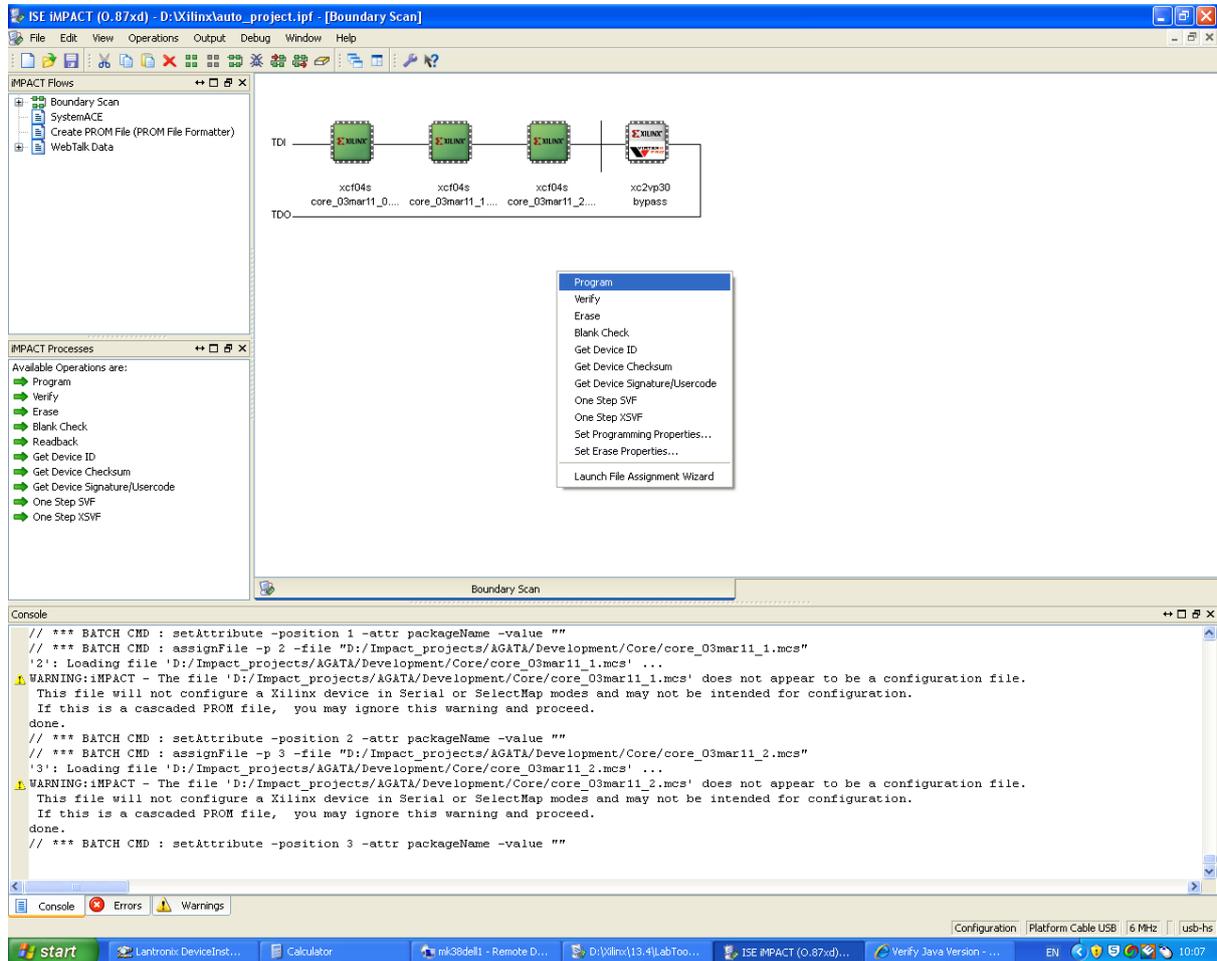
The console window shows the following output:

```
// *** BATCH CMD : setAttribute -position 1 -attr packageN
// *** BATCH CMD : assignFile -p 2 -file "D:/Impact_projec
'2': Loading file 'D:/Impact_projects/AGATA/Development/Co
WARNING:IMPACT - The file 'D:/Impact_projects/AGATA/Develo
This file will not configure a Xilinx device in Serial or
If this is a cascaded PROM file, you may ignore this war
done.
// *** BATCH CMD : setAttribute -position 2 -attr packageN
// *** BATCH CMD : assignFile -p 3 -file "D:/Impact_projec
'3': Loading file 'D:/Impact_projects/AGATA/Development/Co
WARNING:IMPACT - The file 'D:/Impact_projects/AGATA/Development/Core/core_03mar11_2.mcs' does not appear to be a configuration file.
This file will not configure a Xilinx device in Serial or SelectMap modes and may not be intended for configuration.
If this is a cascaded PROM file, you may ignore this warning and proceed.
done.
// *** BATCH CMD : setAttribute -position 3 -attr packageName -value ""
```

7. By using Ctrl and the mouse left click , select the 3 platform flashes , as shown below:



8. Then write click, and select “Program”, as shown below:



9. Wait for programming to finish:

The screenshot displays the ISE IMPACT (O.87xd) interface for a boundary scan project. The main window shows a chain of four devices connected between TDI and TDO pins. The devices are labeled as follows:

- Device 1: xc104s, core_03mar11_0...
- Device 2: xc104s, core_03mar11_1...
- Device 3: xc104s, core_03mar11_2...
- Device 4: xc2vp30, bypass

The left sidebar shows the project structure with folders for Boundary Scan, SystemACE, Create PROM File (PROM File Formatter), and WebTalk Data. Below this, the 'IMPACT Processes' section lists available operations such as Program, Verify, Erase, Blank Check, Readback, and various Get Device operations.

A 'Configuration Operation Status' dialog box is overlaid on the main window, indicating that a command is being executed. The progress bar shows 16% completion, and an 'Abort' button is visible.

The console window at the bottom displays the following log output:

```
'1': Erasure completed successfully.
'1': Programming device...
'1': Programming terminated due to errors.
Operation aborted.
PROGRESS_END - End Operation.
Elapsed time = 6 sec.
INFO:IMPACT - Current time: 29/10/2012 10:11:14
// *** BATCH CMD : Program -p 1 -e -v
PROGRESS_START - Starting Operation.
Maximum TCK operating frequency for this device chain: 15000000.
Validating chain...
Boundary-scan chain validated successfully.
'1': Erasing device...
'1': Erasure completed successfully.
'1': Programming device...
```

The Windows taskbar at the bottom shows the system tray with the time 10:11 and various background applications including 'Lantronix DeviceInst...', 'Calculator', 'mk38del1 - Remote D...', 'D:\Vilinx\13.4\LabToo...', 'ISE IMPACT (O.87xd)...', and 'Verify Java Version - ...'.

10. If programming is successful, the following message appears, as shown below:

